

FCRS should be run first.

When loading via IOCE: TESTS 111 INS

1. IPL loads Go/No-Go.
2. Go/No-Go tests IOCE and then loads Basic Storage Test.
3. Basic Storage Test tests MACH storage and then returns to Go/No-Go for loading of IDM.
4. IDM tests IOCE by sequentially loading and running sections D1003-D1077 and then loads SDM.
5. SDM waits for entry of input messages. Additional sections that can be run, depending on availability of units, are: *BRUNG UPS*
 - D1401 IOCE Internal Timer
 - D1403 IOCE Delay Instruction
 - D1501 IOCE Diagnose Kernel, Part 3
 - D2101 IOCE Local Storage
 - D2308 MACH to Main Storage
 - D2740 MACH Storage
 - D3051-D3155 Channel Tests
 - D4050-D4060 Tape Tests
 - D6251-D6262 2540 Reader Punch
 - D6351-D6356 1403 Printer
 - D6651-D6653 1052 Printer Keyboard
 - D6A51-D6A58 2821 Control Unit
 - D8051-D806A DASF
 - D9051 Reconfiguration Unit
 - DA051 Channel-to-Channel Adapter
 - DB051, DB052 2701 Data Adapter Unit
 - DCC51 PAM and Adapters
 - DCC61 Flight Strip Printer

When loading via CE:

1. IPL loads Go/No-Go. *TESTS 15 INS*
2. Go/No-Go tests CE and then loads Basic Storage Test.
3. Basic Storage Test tests first 128K (decimal) of SE and then returns to Go/No-Go for loading of Hardcore.
4. Hardcore tests CE and then loads SDM. *TESTS 111 INS*
5. SDM waits for entry of input messages. Sections that can be run, depending on availability of units are: *BRUNG UPS*
 - D1101-D1103 Basic CE Test
 - D1108 Basic Diag and Logout
 - D1111-D1115 CEDA
 - D1151-D1308 CE Function Tests
 - D13CD CE Random
 - D1DA3 Direct Control
 - D22A0-D22AA SE and DE Storage
 - D3051-DCC61 Same as left but also including D6CA6 7265-03 Config Console
6. Enter LMDM/ message to SDM at any time to load MDM-D/E. All the above listed sections plus the following run under MDM-D/E: *MDM WAITS FOR ENTRY OF IP MESSAGE*
 - D24A0 DE/DG Interface
 - D46A0 TCU Dual Interface
 - D6AA0 2821 Dual Interface
 - D6CA4 7265-02 System Console
 - D80A0 DASF Two Channel Sw
 - DB0A1 DAU Two-Processor Sw
 - DCCA0 PAM Dual Interface
 - DD6A2 DAR/DAR Mask
 - DD8A0 Configuration Control
 - DD9A0 ATR Controls
 - DDAA0 SSU Multi-Element
 - DDDA1 IOCE Processor
 - DE0A3-DE5CA SEVA Program

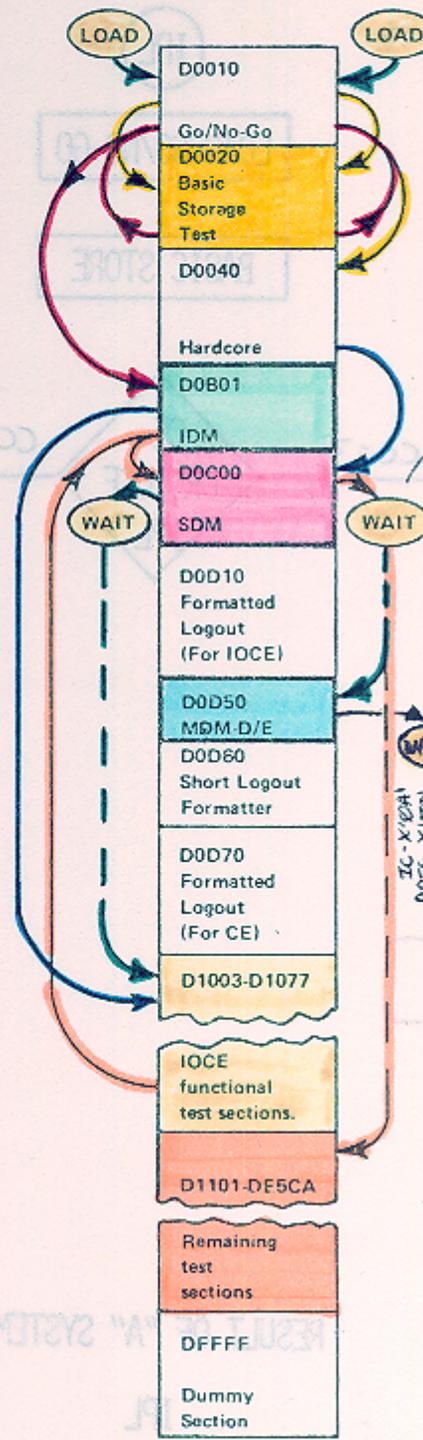


FIGURE 1-1. OPERATION OF SYSTEM MAINTENANCE TAPE (PART 2 OF 2)